FIG.1

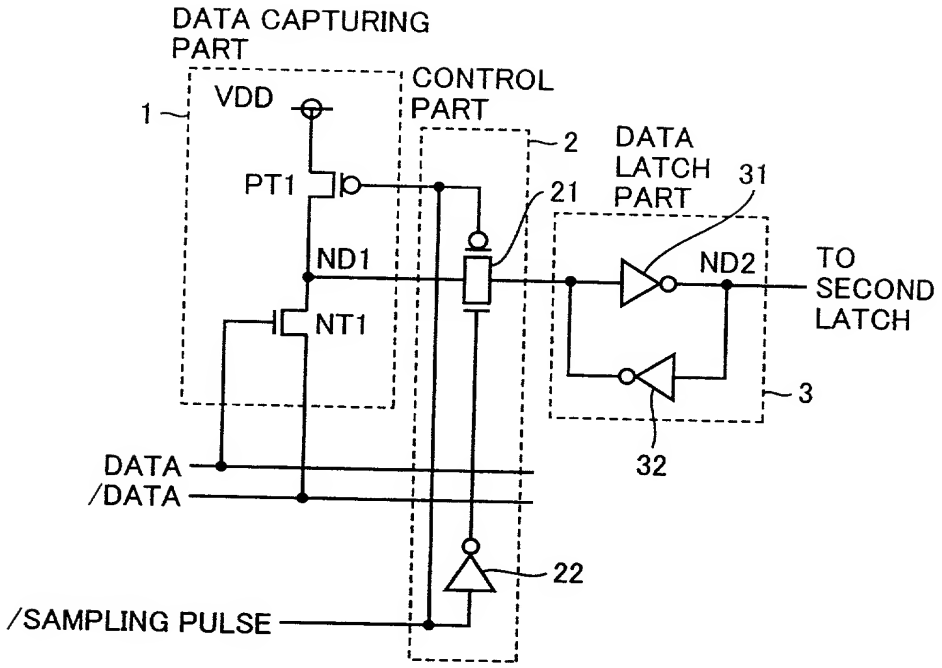
DATA CAPTURING PART
CONTROL PART
DATA LATCH PART
VDD
1
2
PT1
21
31
ND1
ND2
TO SECOND LATCH
NT1
3
32
DATA
/DATA
22
/SAMPLING PULSE

FIG.2

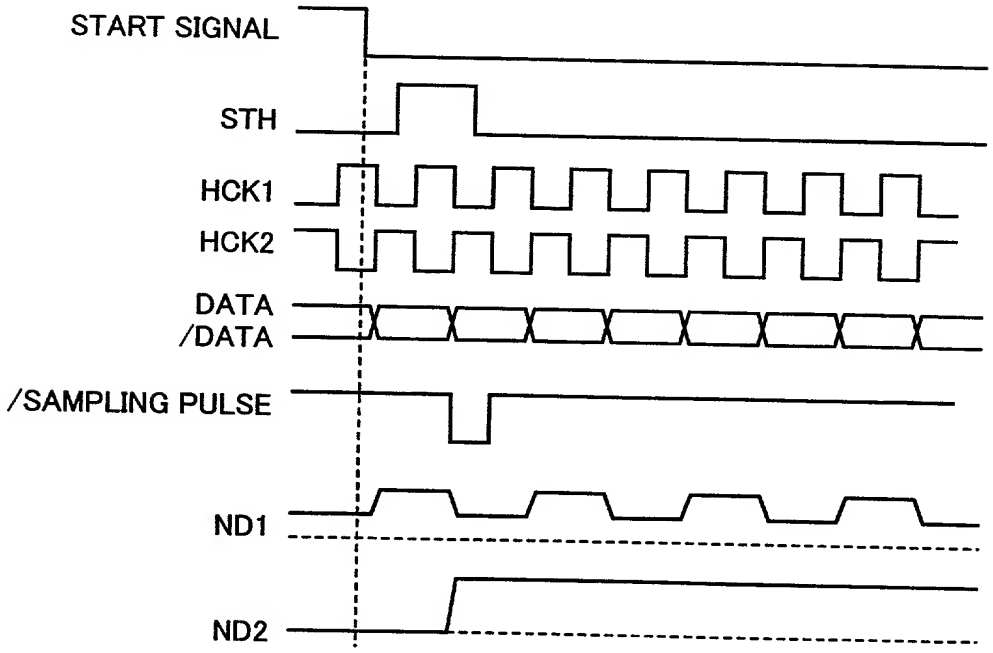
START SIGNAL
STH
HCK1
HCK2
DATA
/DATA
/SAMPLING PULSE
ND1
ND2

FIG.3

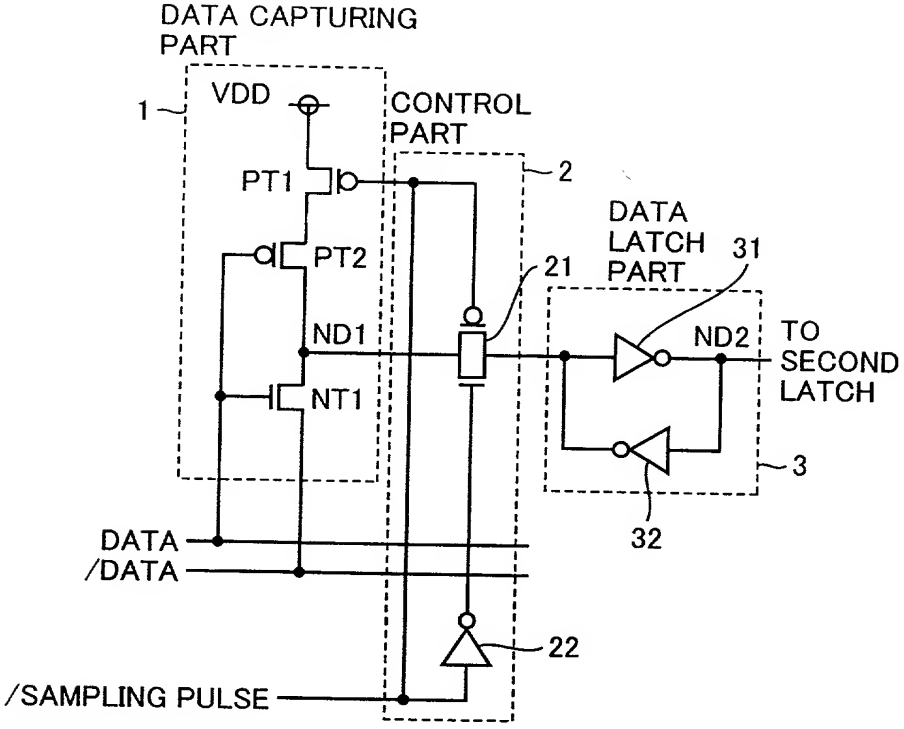
DATA CAPTURING PART
1
VDD
CONTROL PART
2
PT1
PT2
ND1
NT1
DATA LATCH PART
31
21
ND2
TO SECOND LATCH
3
32
DATA
/DATA
22
/SAMPLING PULSE

FIG.4

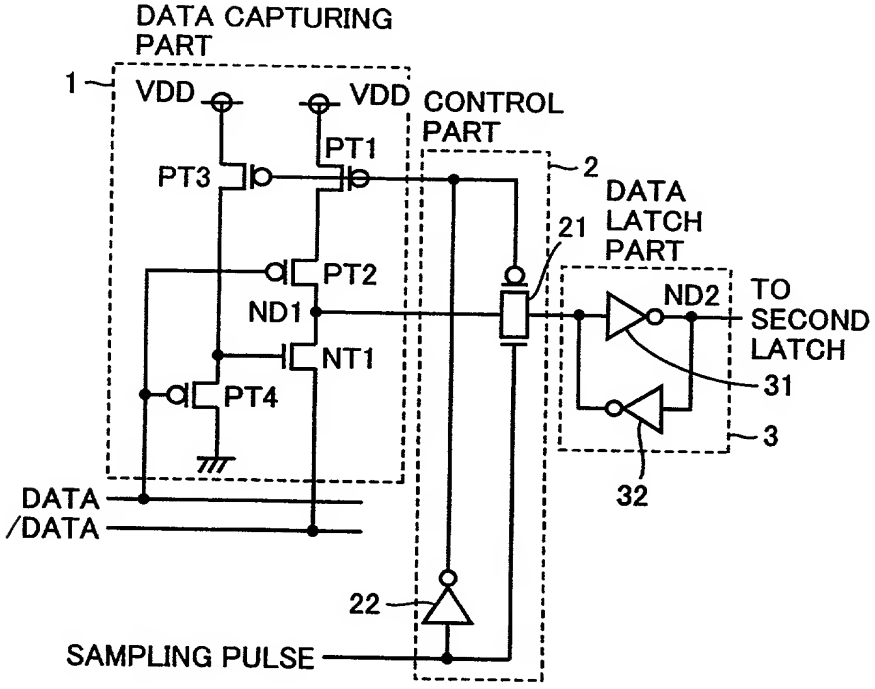
DATA CAPTURING PART
1
VDD
VDD
CONTROL PART
2
PT1
PT3
PT2
ND1
NT1
PT4
DATA LATCH PART
21
ND2
TO SECOND LATCH
31
3
32
DATA
/DATA
22
SAMPLING PULSE

FIG.5

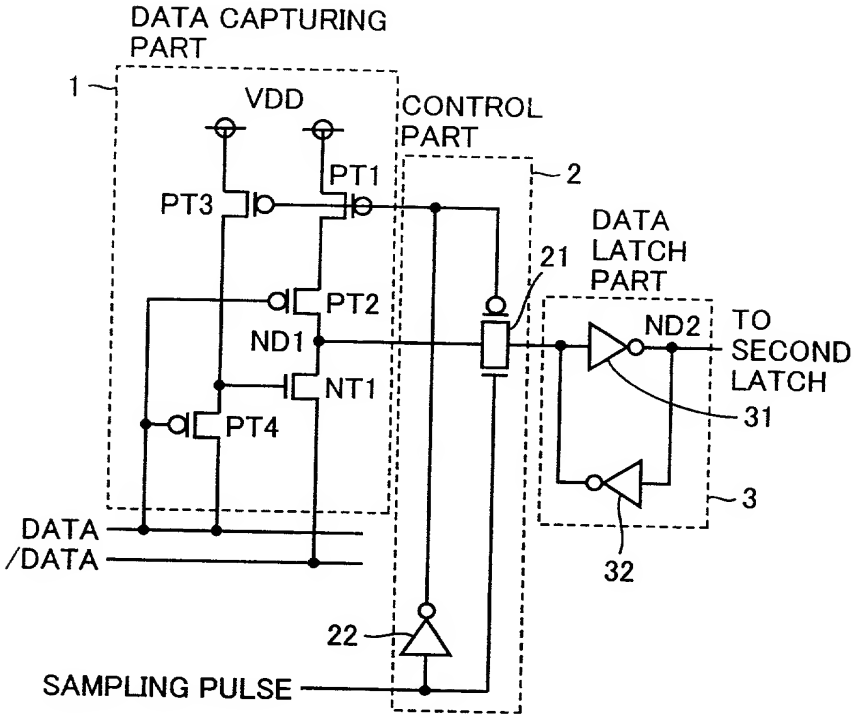
DATA CAPTURING PART
1
VDD
CONTROL PART
2
PT1
PT3
PT2
ND1
NT1
PT4
21
DATA LATCH PART
ND2
TO SECOND LATCH
31
3
32
DATA
/DATA
22
SAMPLING PULSE

FIG.6

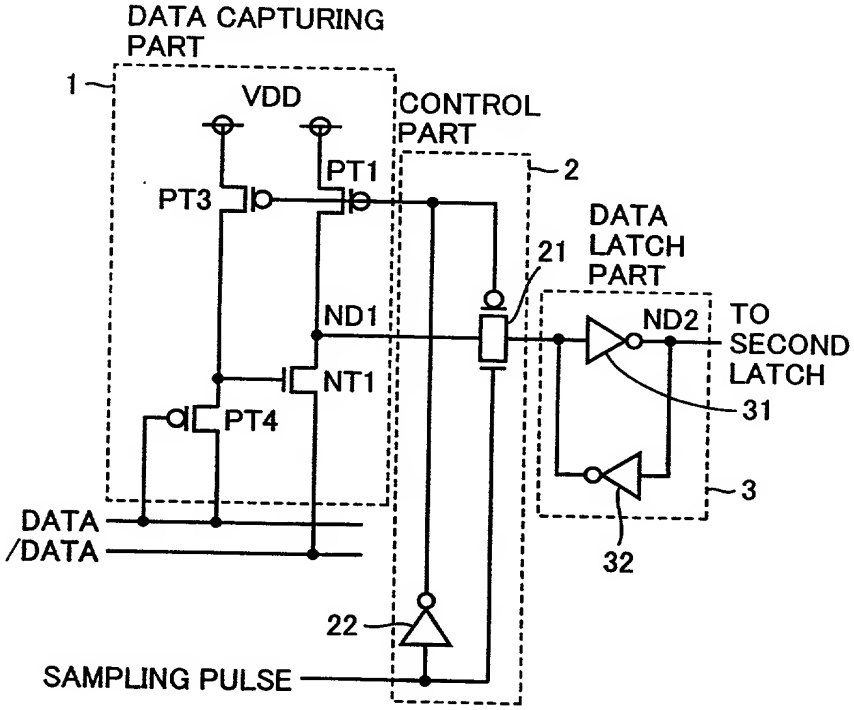
DATA CAPTURING PART
1
VDD
CONTROL PART
2
PT1
PT3
21
DATA LATCH PART
ND1
ND2
TO SECOND LATCH
NT1
31
PT4
3
32
DATA
/DATA
22
SAMPLING PULSE

FIG.7

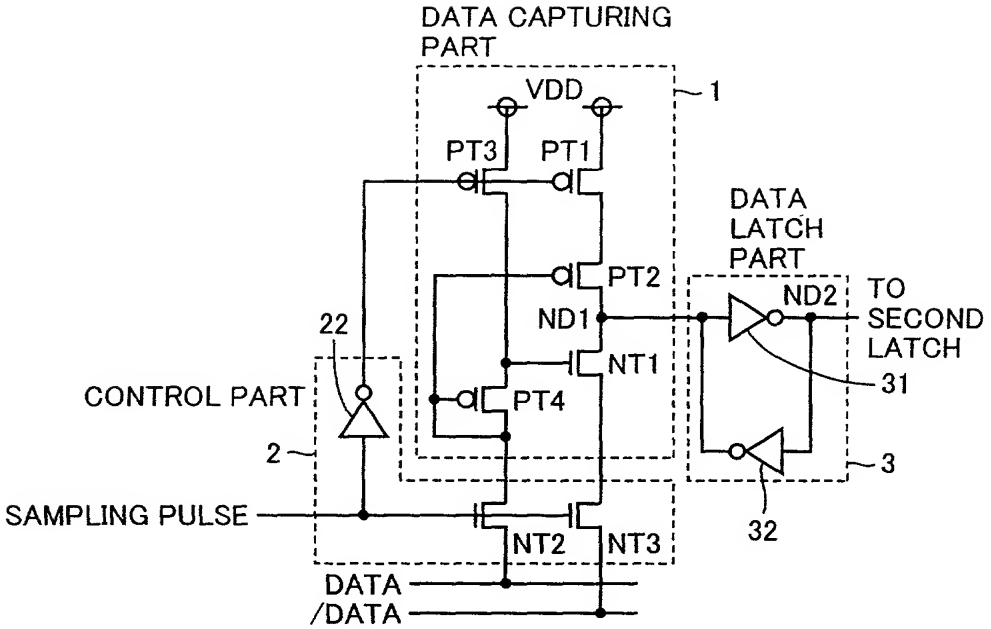
DATA CAPTURING PART
VDD
1
PT3
PT1
DATA LATCH PART
PT2
ND2
TO SECOND LATCH
22
ND1
NT1
31
CONTROL PART
PT4
2
3
SAMPLING PULSE
NT2
NT3
32
DATA
/DATA

FIG.8

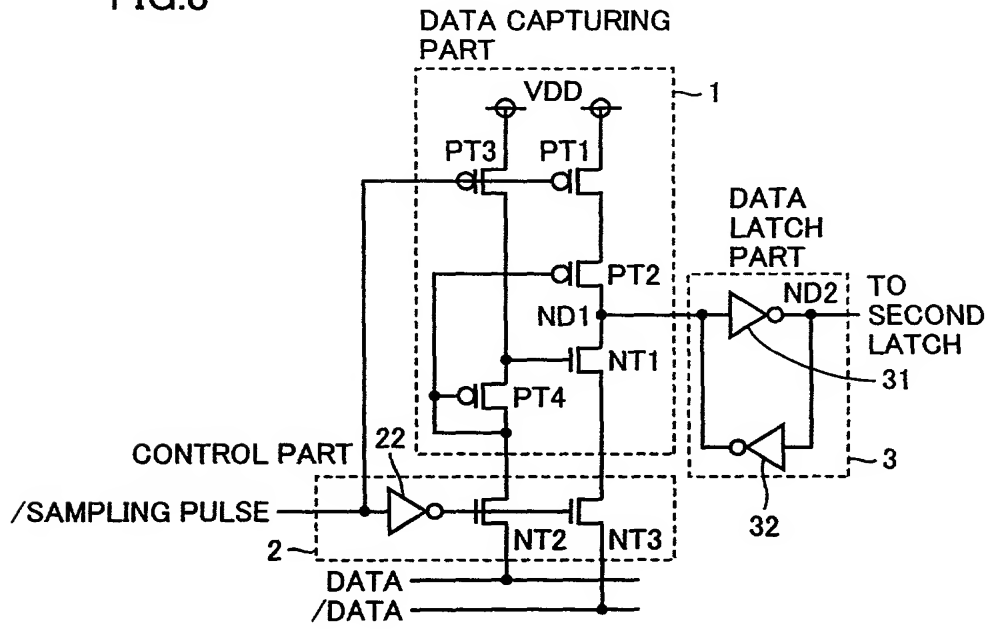
DATA CAPTURING PART
VDD
1
PT3
PT1
DATA LATCH PART
PT2
ND2
TO SECOND LATCH
ND1
NT1
31
PT4
CONTROL PART
22
3
/SAMPLING PULSE
32
2
NT2
NT3
DATA
/DATA

FIG.9

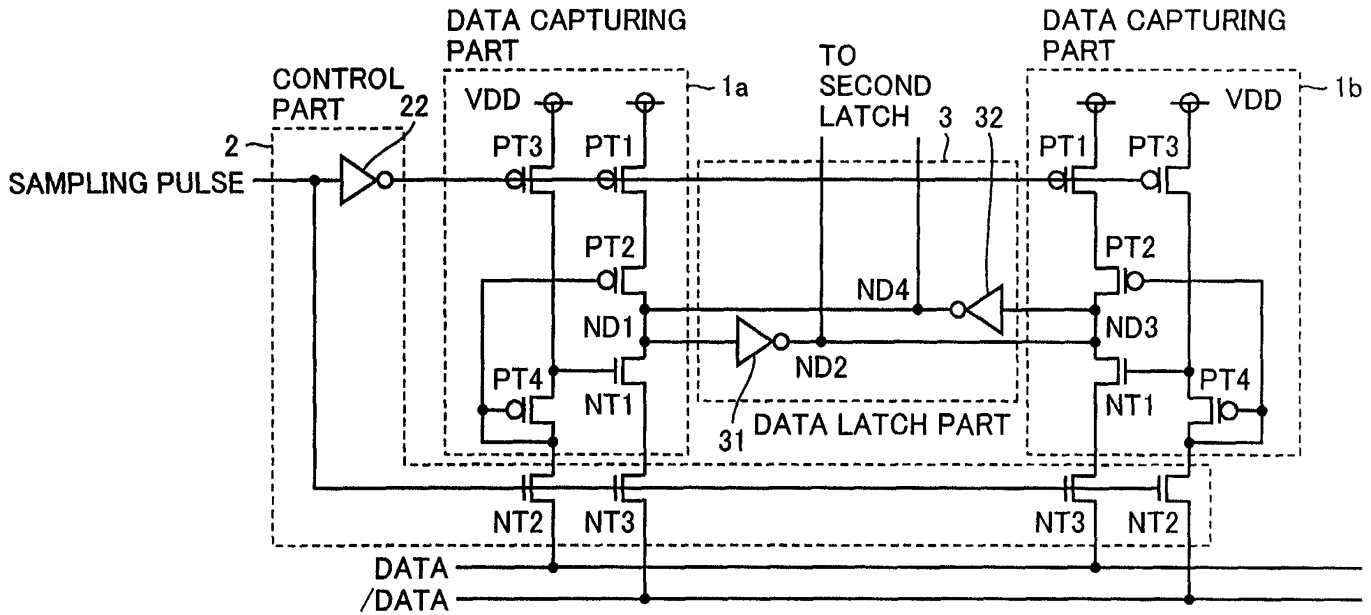
DATA CAPTURING PART
CONTROL PART
22
2
SAMPLING PULSE
VDD
1a
TO SECOND LATCH
3
32
DATA CAPTURING PART
VDD
1b
PT3
PT1
PT2
ND1
PT4
NT1
ND2
ND4
ND3
31
DATA LATCH PART
NT2
NT3
DATA
/DATA

FIG.10 PRIOR ART

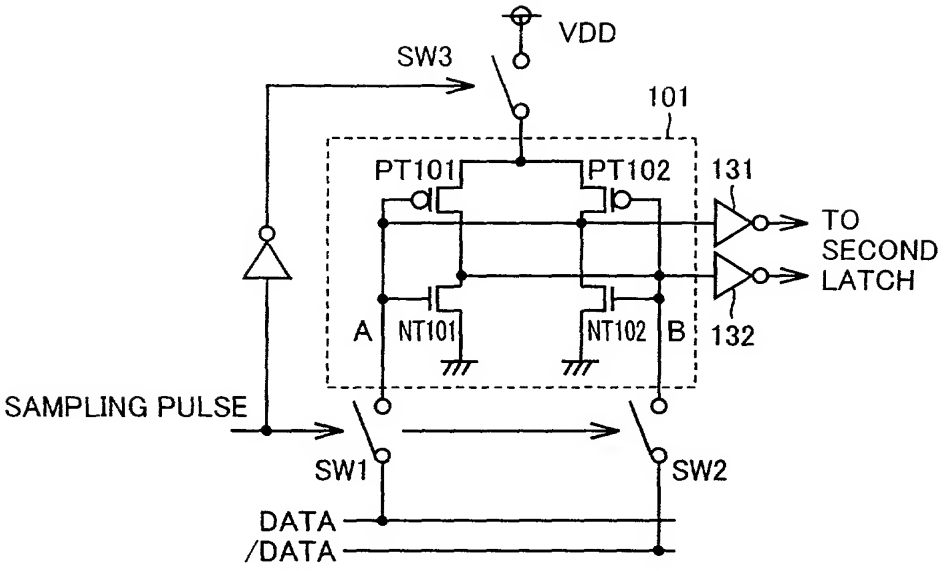
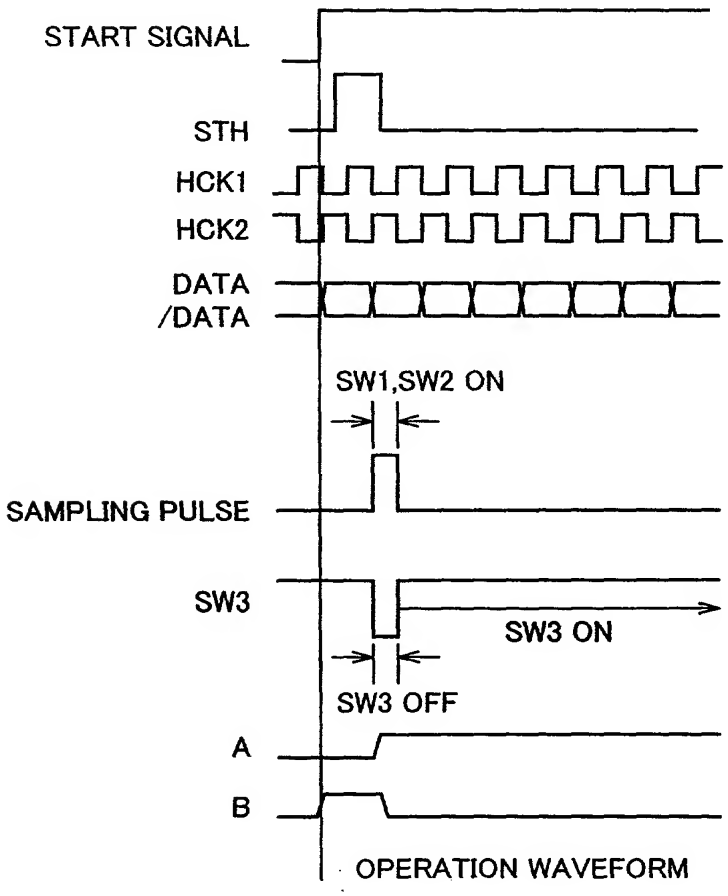
FIG.11 PRIOR ART